IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: STICKLER, ET AL.

Serial No.: 09/736,615

Filed: December 14, 2000

For: PRINTED CIRCUIT BOARD LAYOUT

Atty Dkt: HP-10007356-1

Group Art Unit: 2841

Examiner: J. Alcala

DECLARATION OF LISA ANN CASELLI

I, Lisa Ann Caselli, hereby declare:

1. I hold the degree of Bachelor of Science in Electrical Engineering from Purdue University.

2. I have been an employee of Hewlett-Packard Company for over eight years.

3. My current position at Hewlett-Packard Company, which I have held since 1996, is Hardware Design Engineer.

4. I am one of the named inventors on the above described patent application.

5. I performed a number of tests on single-ended SCSI buses on a printed circuit system board (PCSB) of a Sun Microsystems Inc. Ultra 60 Workstation.

6. The tests were conducted to determine the location of certain signal traces.

7. After conducting the tests I photographed the PCSB and provided annotations on printouts of those photographs which identify the location of certain signal traces.

8. Those photographs with my annotations accurately describe the results of my tests and are attached hereto as Appendix I.

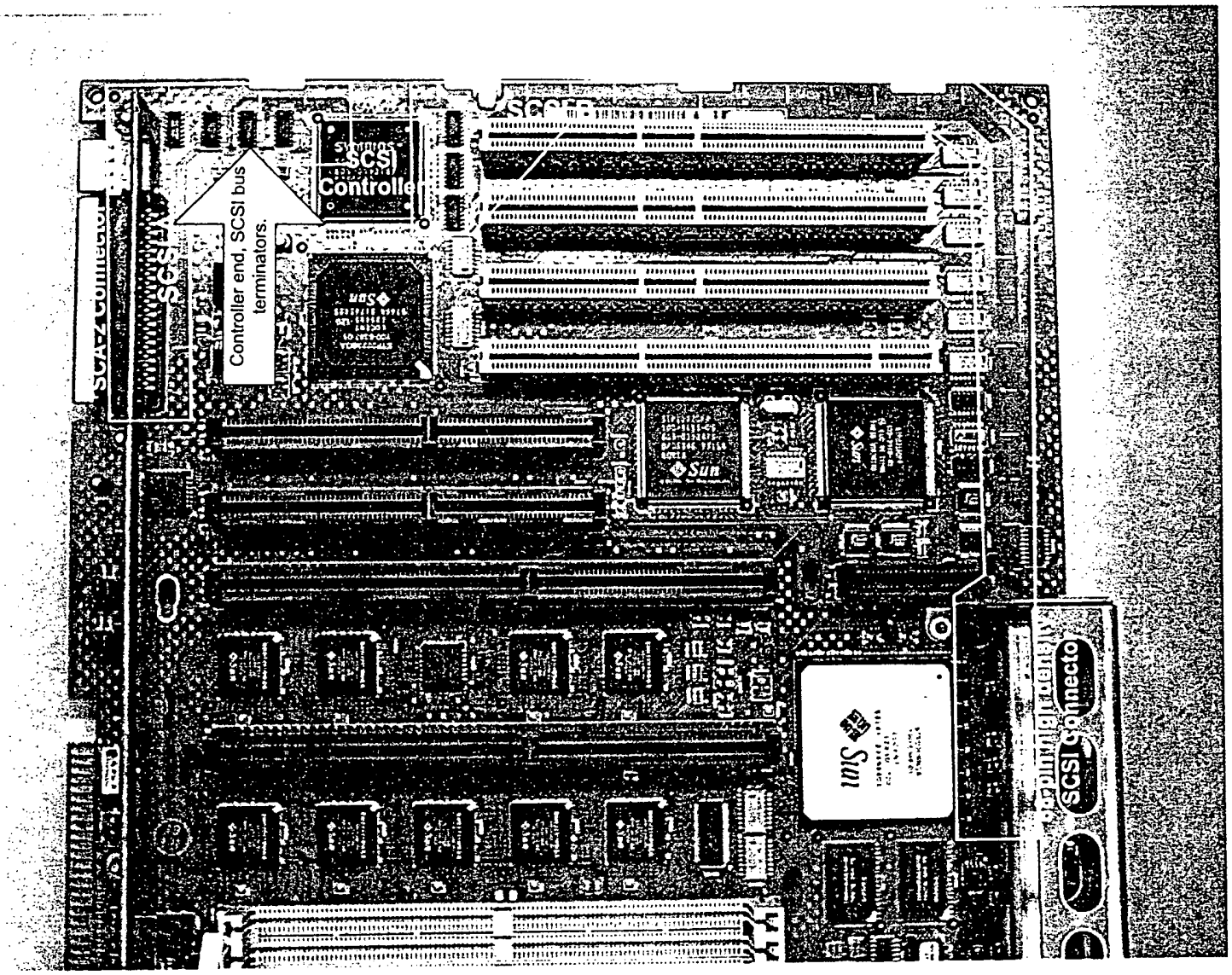
I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application, any patent issued thereon, or any patent to which this declaration is directed.

Lisa Ann Caselli

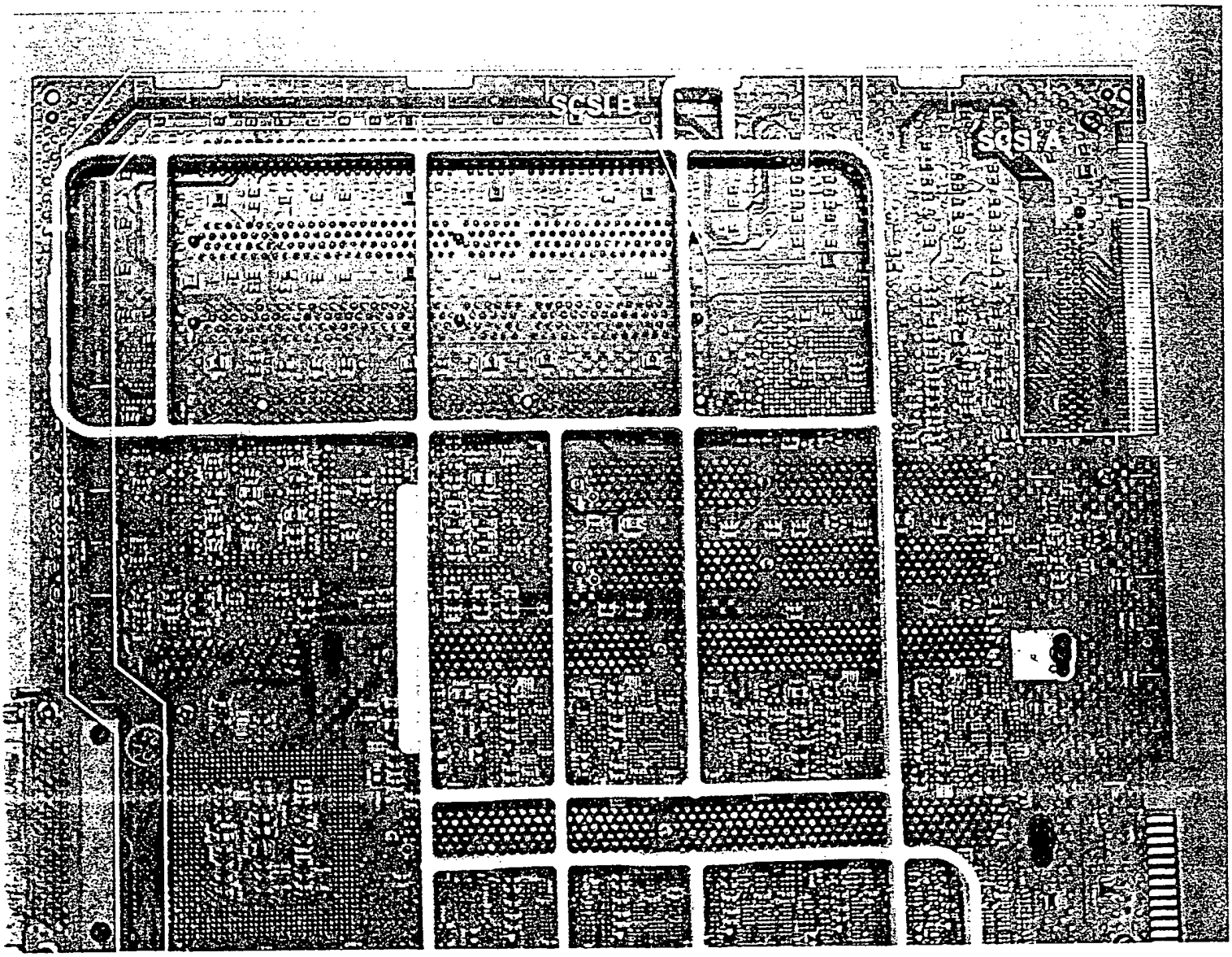
Lisa Ann Caselli

5/30/02

Date

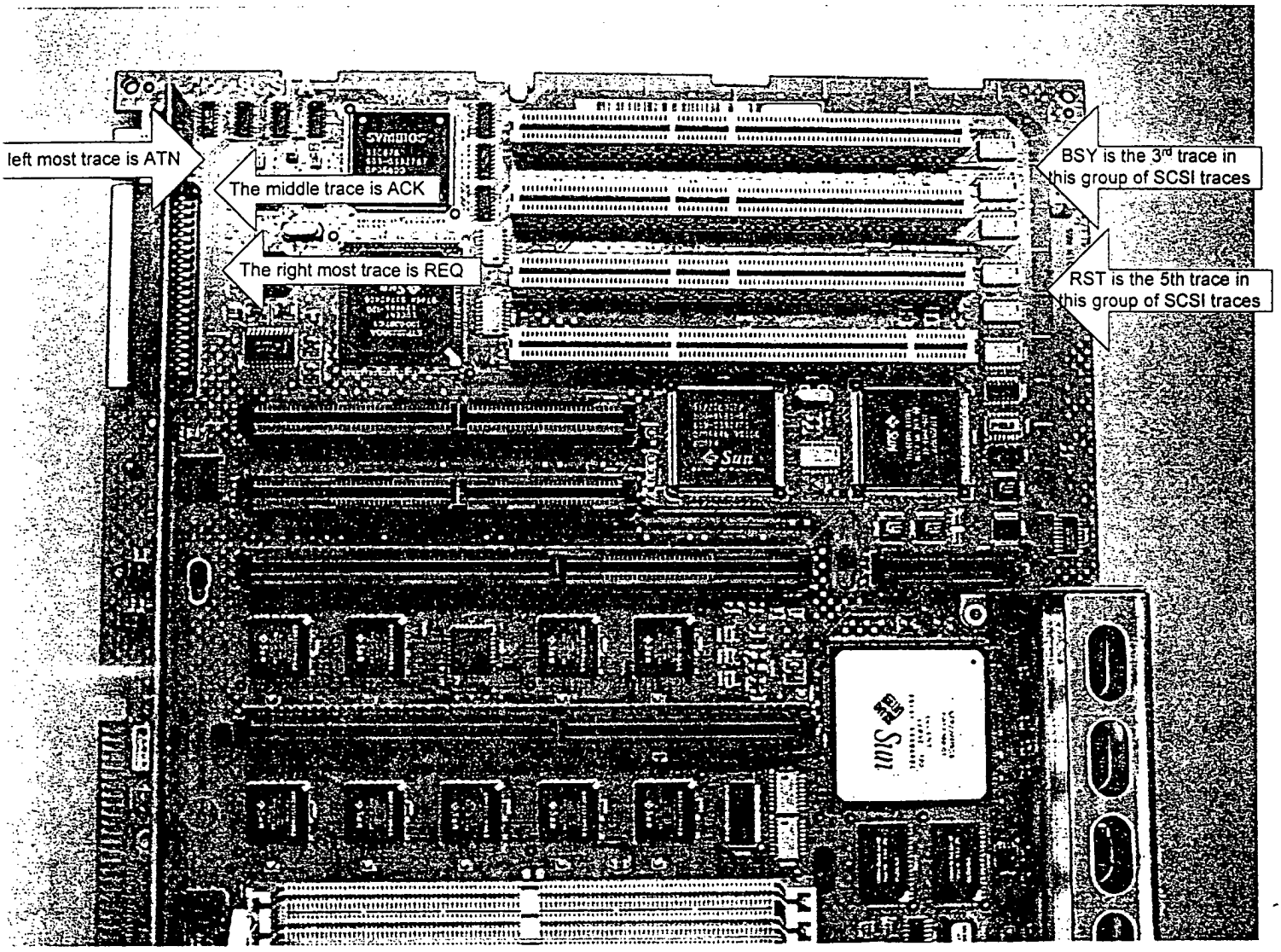


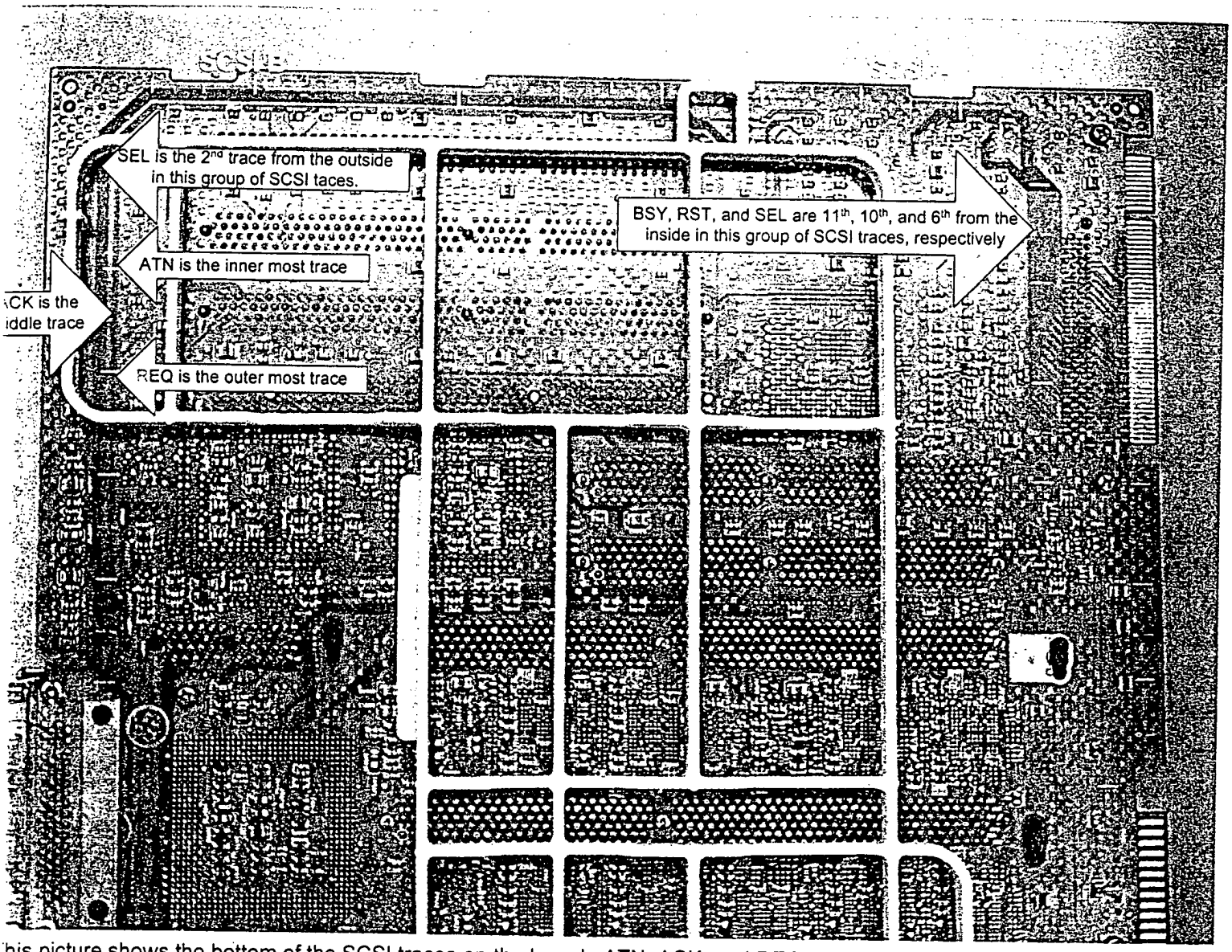
This Sun Ultra 60 workstation shown above has two ultra, wide, single-ended SCSI busses provided by the Symbios (now LSI) 53C876 dual channel SCSI host adapter. These busses will be referred to in this document as SCSI A and SCSI B. The SCSI traces are routed on outer layers of the board. This picture shows the traces on the topside. The next photo will show the traces on the bottom of the board.



The above photo shows the backside of the Sun Ultra 60 Workstation and outlines the backside traces of SCSI A and SCSI B.

Both SCSI A and SCSI B isolate three traces from the rest of the SCSI bus. These SCSI signals are ATN (attention), ACK (acknowledge), and REQ (request). In SCSI A, ATN, ACK, and REQ are isolated on the top of the board with all other SCSI traces on the bottom of the board. In SCSI B, the signals are isolated on the bottom side of the board with SCSI traces running on both the top and the bottom. The traces on the bottom are separated with additional space from ATN, ACK, and REQ. The photo below shows the top side of the board and points out the isolated signals in SCSI A and SCSI B. The next page will show the signals that are on the bottom.





his picture shows the bottom of the SCSI traces on the board. ATN, ACK, and REQ in SCSI B are separate from other SCSI aces by some distance.